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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Jay B. Reimer, et al.

TI-30105

Art Unit: 2111

Serial No.: 10/008,696

Examiner: Khanh Dang

Filed: October 30, 2001

Conf. No.: 7469

For: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA

Access

CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

December 2, 2004
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>116</u> (<u>8</u> Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u> </u> (# Page)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	<input checked="" type="checkbox"/> Amt Fee TL (1 page)
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Jay B. Reimer, et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/008,696 Filing Date: October 30, 2001	
TITLE OF INVENTION: Multicore DSP Device Having Coupled Subsystem Memory Buses for Global DMA Access	
TI FILE NO.: TI-30105	DEPOSIT ACCT. NO.: 20-0668
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Texas Instruments Incorporated
PO Box 655474, M/S 3999
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RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2111

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Reimer et al

Art Unit: 2111

Serial No.: 10/008,696

Examiner: Khanh Dang

Filed: November 8, 2001

Docket: TI-30105

For: MULTICORE DSP DEVICE HAVING COUPLED SUBSYSTEM MEMORY BUSES FOR
GLOBAL DMA ACCESS

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FAX TRANSMITTAL
UNDER 37 C.F.R. §1.6(b)

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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated October 4, 2004. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: